Development & Innovation Roadmap

IBM **Quantum**

New Development & Innovation Roadmap →

Development Roadmap

IBM **Quantum**

	2016–2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2033+
	Ran quantum circuits on the IBM Quantum Platform	Released multi-dimensional roadmap publicly with initial aim focused on scaling	Enhanced quantum execution speed by 100x with Qiskit Runtime	Brought dynamic circuits to unlock more computations	Enhanced quantum execution speed by 5x with Quantum Serverless and execution modes	Improve quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhance quantum execution speed and parallelization with partitioning and quantum modularity	Improve quantum circuit quality to allow 7.5K gates	Improve quantum circuit quality to allow 10K gates	Improve quantum circuit quality to allow 15K gates	Improve quantum circuit quality to allow 100M gates	Beyond 2033, quantum-centric supercomputers will include 1000's of logical qubits unlocking the full power of quantum computing
Data scientists						Platform						
						Code assistant	Functions	Mapping collections	Specific libraries			General purpose QC libraries
Researchers					Middleware							
					Quantum Serverless	Transpiler service	Resource management	Circuit knitting x p	Intelligent orchestration			Circuit libraries
Quantum physicists			Qiskit Runtime									
	IBM Quantum Experience		QASM 3	Dynamic circuits	Execution modes	Heron (5K)	Flamingo (5K)	Flamingo (7.5K)	Flamingo (10K)	Flamingo (15K)	Starling (100M)	Blue Jay (1B)
	Early Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	**Falcon** Benchmarking 27 qubits		**Eagle** Benchmarking 127 qubits		Error mitigation 5k gates 133 qubits Classical modular 133x3 = 399 qubits	Error mitigation 5k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 7.5k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 10k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 15k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error correction 100M gates 200 qubits Error corrected modularity	Error correction 1B gates 2000 qubits Error corrected modularity

Innovation Roadmap

Software innovation	IBM Quantum Experience	Qiskit Circuit and operator API with compilation to multiple targets	Application modules Modules for domain specific application and algorithm workflows	Qiskit Runtime Performance and abstraction through primitives	Quantum Serverless Demonstrate concepts of quantum-centric supercomputing	AI-enhanced quantum Prototype demonstrations of AI-enhanced circuit transpilation	Resource management System partitioning to enable parallel execution	Scalable circuit knitting Circuit partitioning with classical reconstruction at HPC scale	Error correction decoder Demonstration of a quantum system with real-time error correction decoder			
Hardware innovation	**Early** Canary 5 qubits Penguin 20 qubits Albatross 16 qubits Prototype 53 qubits	**Falcon** Demonstrate scaling with I/O routing with bump bonds	**Hummingbird** Demonstrate scaling with multiplexing readout	**Eagle** Demonstrate scaling with MLW and TSV	**Osprey** Enabling scaling with high density signal delivery	**Condor** Single system scaling and fridge capacity	**Flamingo** Demonstrate scaling with modular connectors	**Kookaburra** Demonstrate scaling with nonlocal c-coupler Demonstrate path to improved quality with logical memory	**Cockatoo** Demonstrate path to improved quality with logical communication	**Starling** Demonstrate path to improved quality with logical gates		
						Heron Architecture based on tunable-couplers	**Crossbill** Demonstrate m-couplers					

IBM Quantum / © 2024 IBM Corporation

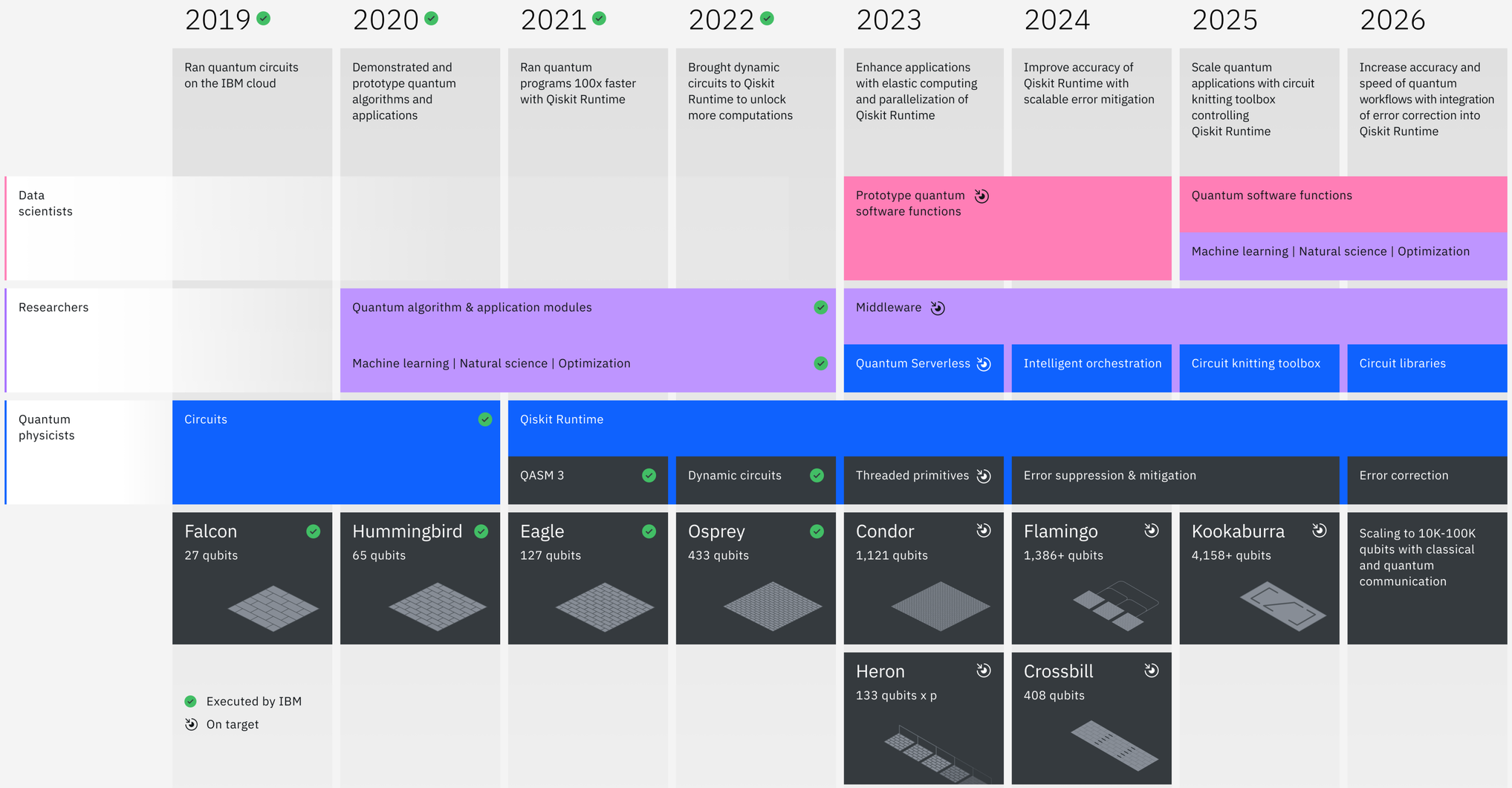
Evolution of our roadmap →

Previous development roadmap

IBM **Quantum**

Back in 2020, IBM released an ambitious roadmap for maturing and scaling quantum technology.

This roadmap set us on course to deliver a 1,000+ qubit chip in just three years while developing software and services necessary to run applications on quantum systems.



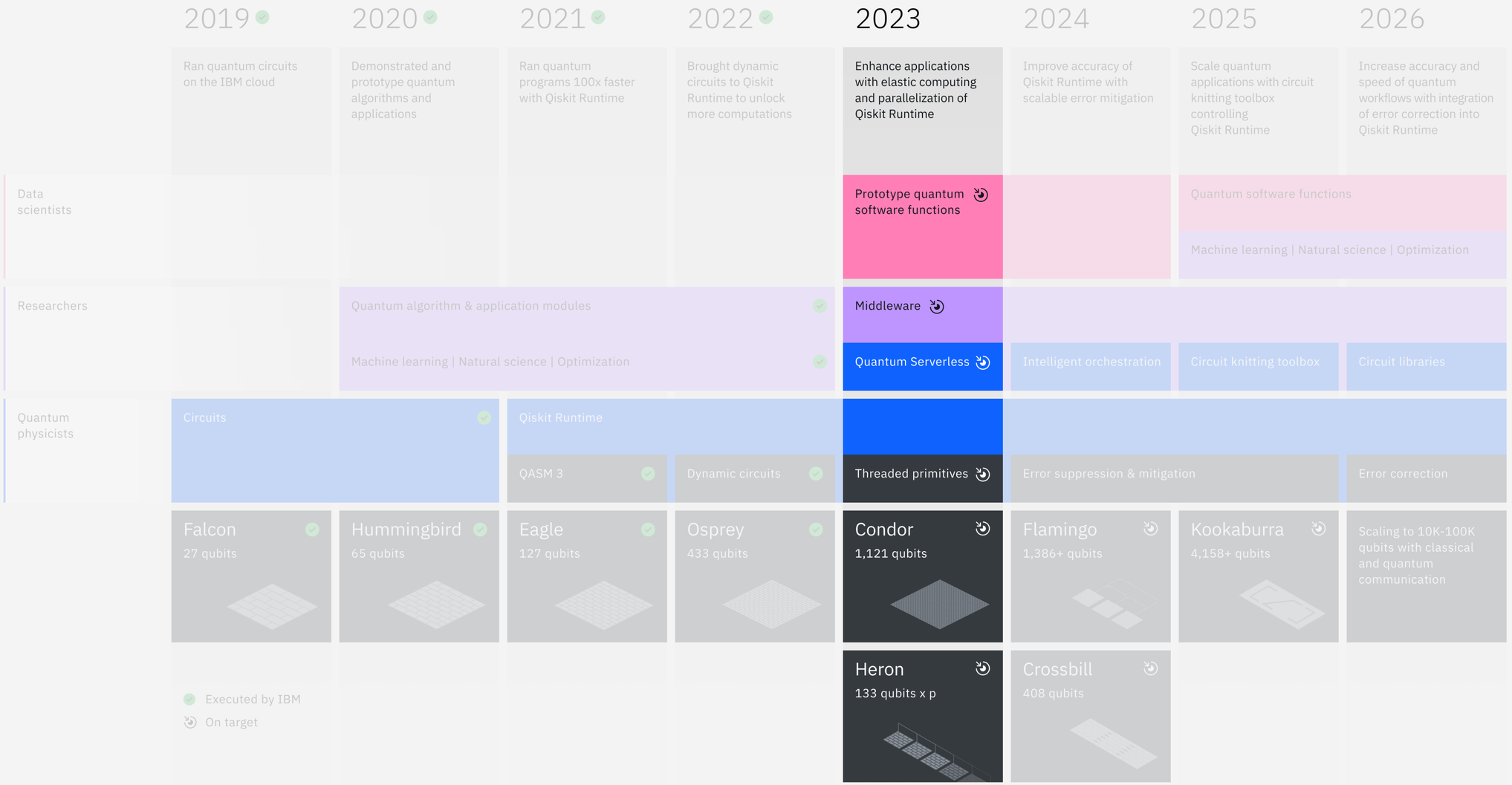
IBM Quantum / © 2024 IBM Corporation

Previous development roadmap

IBM **Quantum**

By 2023, our research and development work made it possible to use quantum computers as tools to run circuits beyond the reach of brute-force classical computation. We could also begin thinking about implementing error correction.

We realized: it was time for a bigger roadmap.



IBM Quantum / © 2024 IBM Corporation

Updated development roadmap

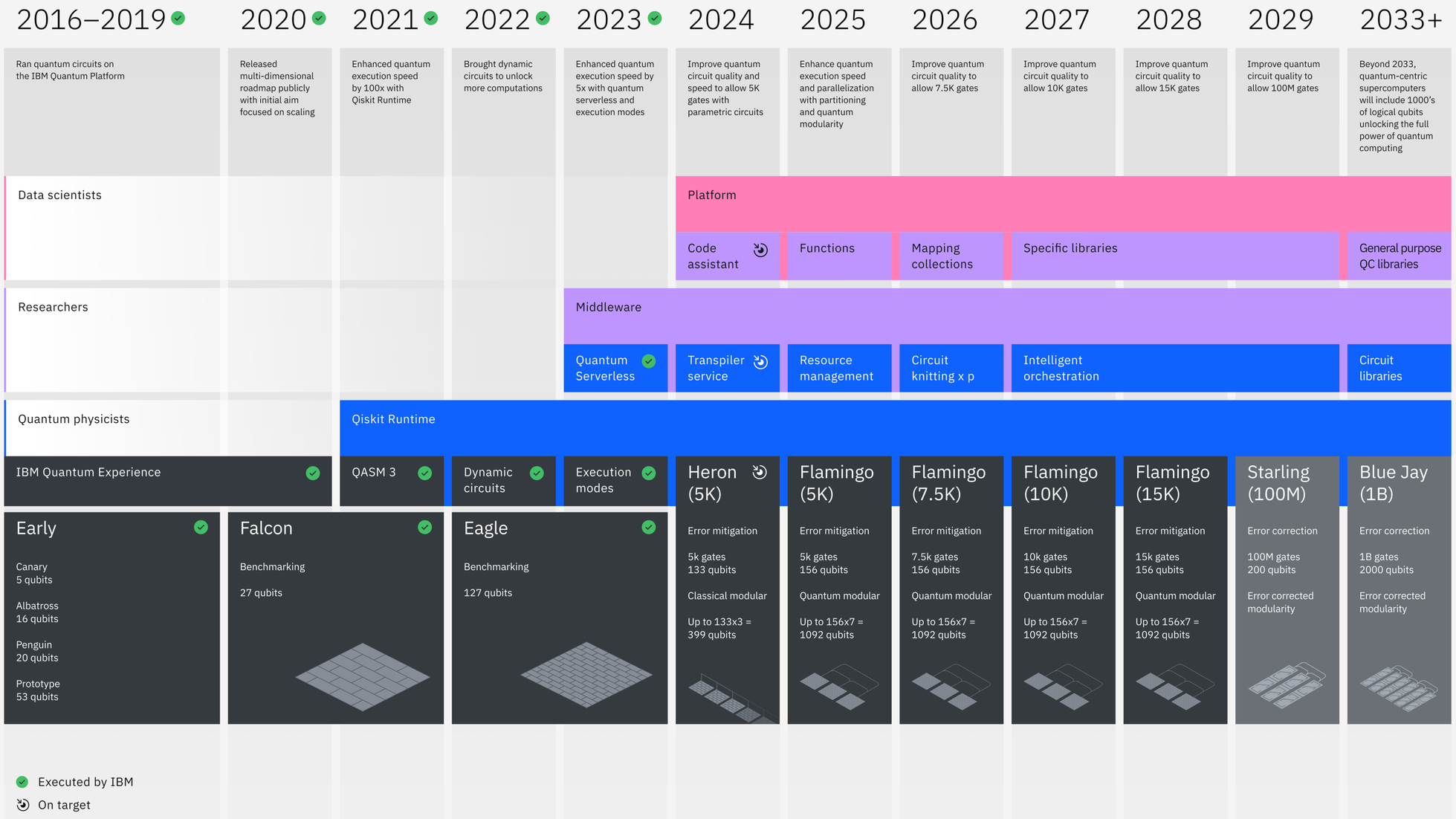
IBM **Quantum**

Our updated development roadmap charts our course for delivering client-facing systems and services. It now focuses both on qubit count and on the size of the circuits that our systems can run, tracked by the number of gates in those circuits.

You can start exploring quantum utility today, and this roadmap shows how the quantum workload size available for that exploration will increase.

Our challenge is to develop the tools that users need to explore quantum utility and unlock the full power of quantum-centric supercomputing by 2033.

We will also incorporate advances in machine learning and generative AI to turbocharge our software's performance.



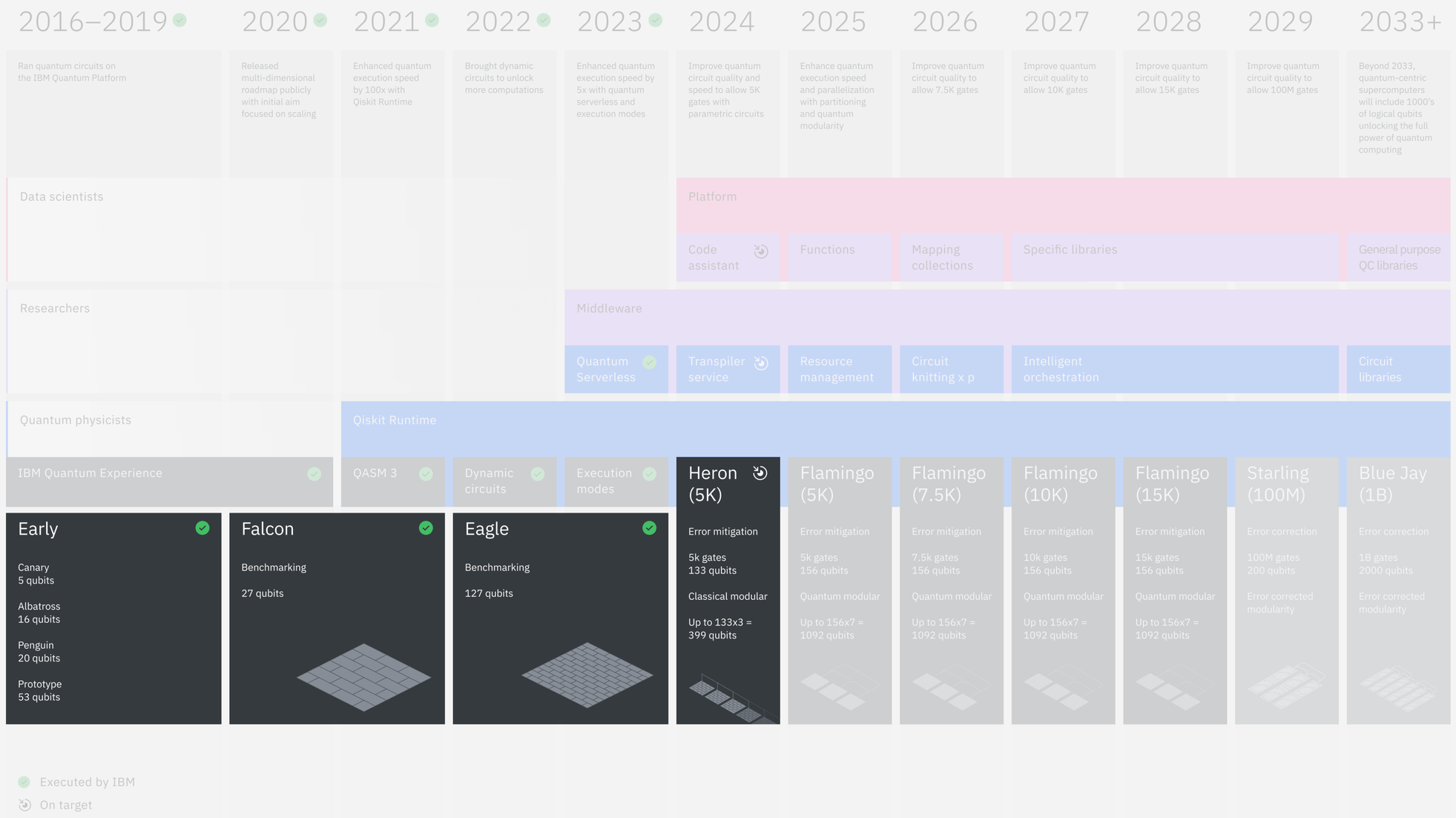
IBM Quantum / © 2024 IBM Corporation

What we have accomplished: Hardware

IBM **Quantum**

From 2020 to 2023, we focused on solving single-chip scaling with the IBM Quantum Falcon, Hummingbird, Eagle, Osprey, and Condor chips.

In 2023, we debuted the IBM Quantum Heron chip, which uses tunable couplers to achieve our lowest error rates yet. Heron will serve as the basis for modular scaling of quantum processors. In 2024, Heron will be capable of running 5,000 gates.



IBM Quantum / © 2024 IBM Corporation

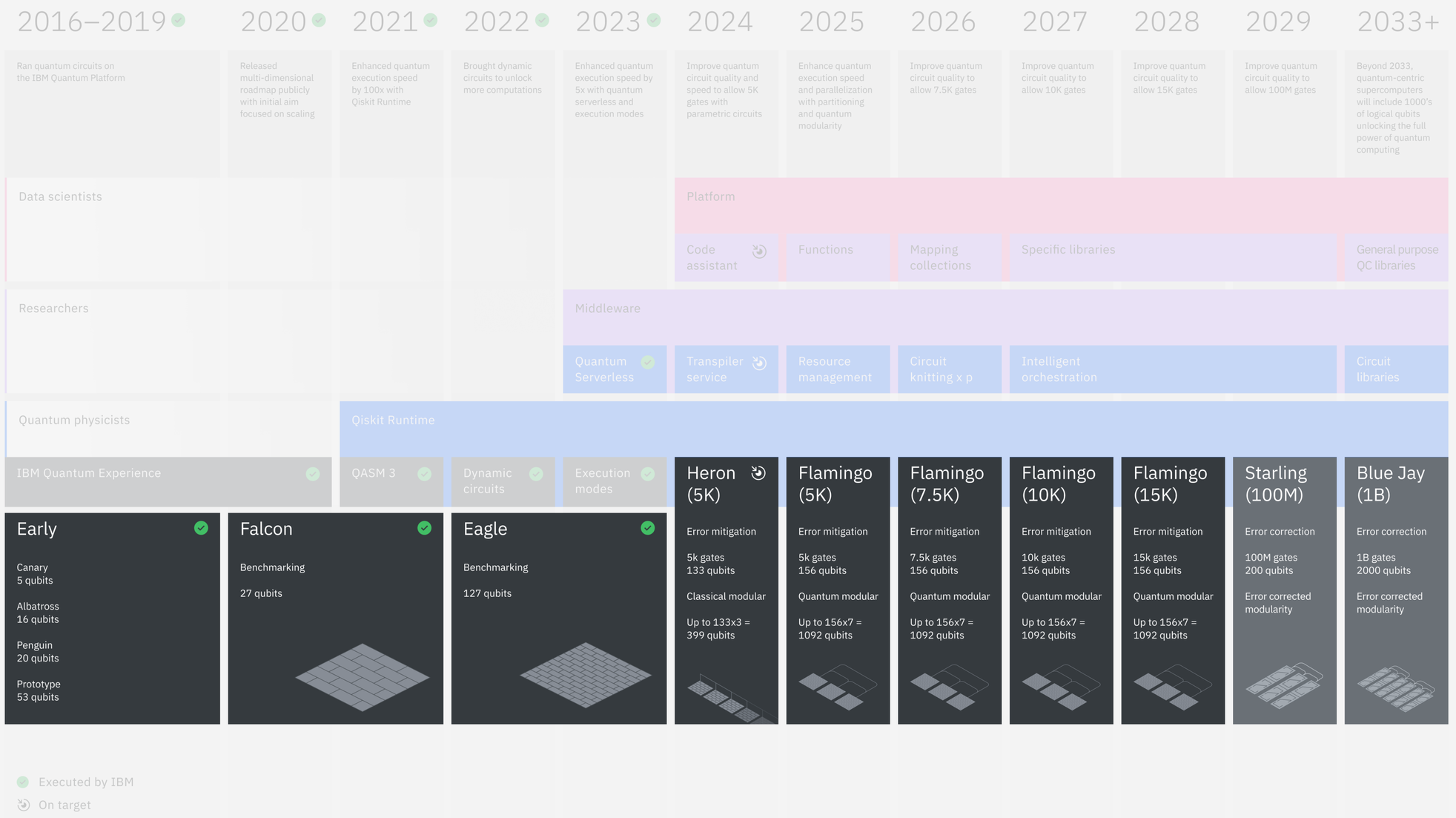
Looking forward: Hardware

IBM **Quantum**

Now, we use error mitigation and interconnects to run larger circuits so users can look for quantum advantages in their domains. Through classical and quantum modularity, we plan to achieve an IBM Quantum Flamingo system capable of running 15,000 gates with the help of error mitigation by 2028.

We foresee advances in quantum error correction allowing us to debut IBM Quantum Starling, a system capable of running circuits with 100 million gates on 200 logical qubits, by 2029. In 2033, we will debut IBM Quantum Blue Jay, a system capable of running circuits with a billion gates on 2,000 logical qubits.

As we roll out error correction, developers need not change how they write quantum programs. They will simply notice that they can run longer workloads.



IBM Quantum / © 2024 IBM Corporation

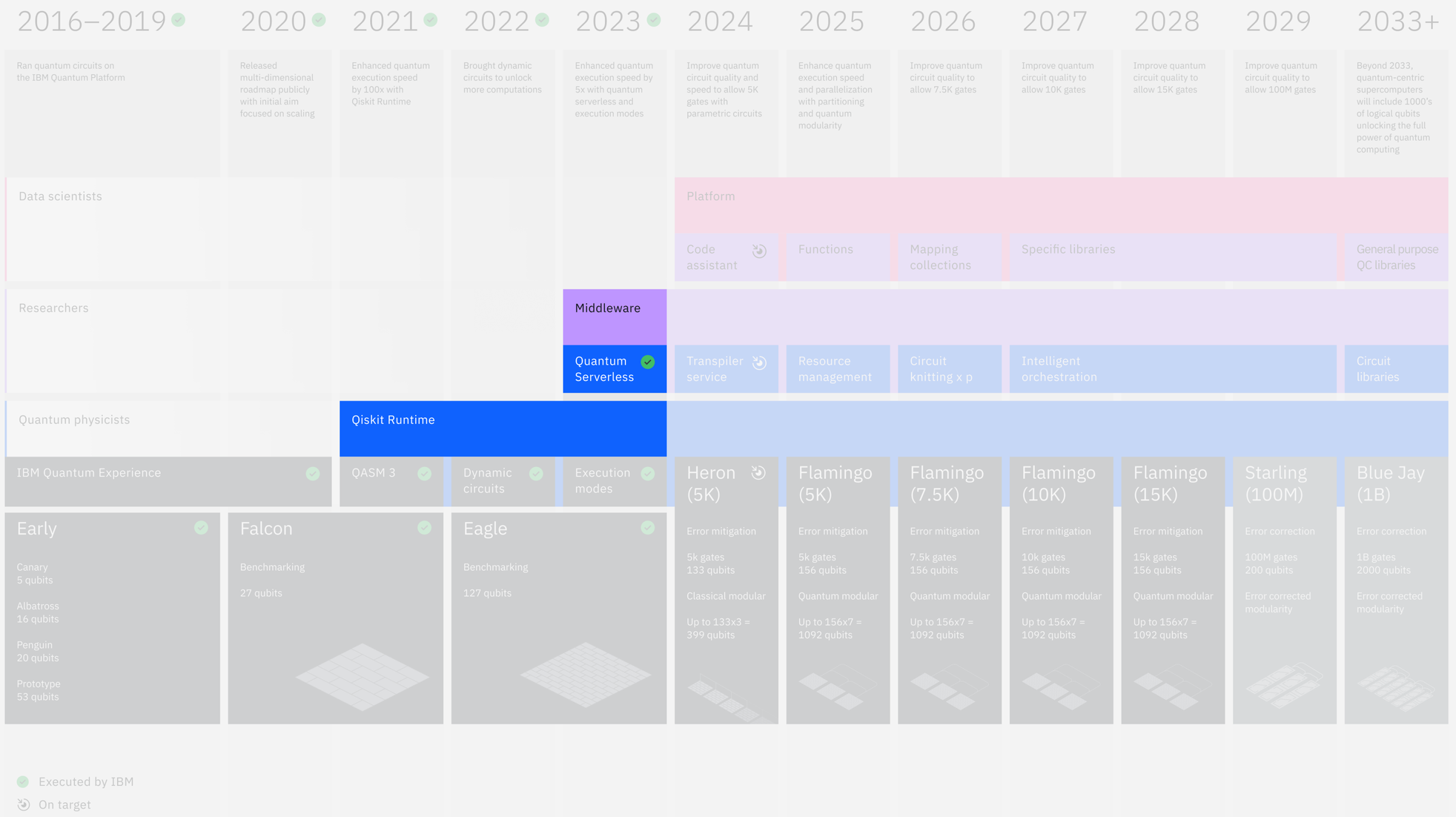
What we have accomplished: Execution and orchestration

Running quantum workloads requires infrastructure that coordinates quantum resources with near-time and real-time classical resources.

Since 2016, we have worked to create Qiskit and a variety of application libraries to show our users what coding a quantum computer looks like.

In 2021, we released Qiskit Runtime, a service allowing users to orchestrate their programs across IBM Quantum processors and the cloud.

In 2023, we introduced middleware for quantum tools to automate and optimize heterogeneous compute tasks. That included quantum serverless to provision users the exact quantum resources they need, when they need them.



Looking forward:
Execution and orchestration

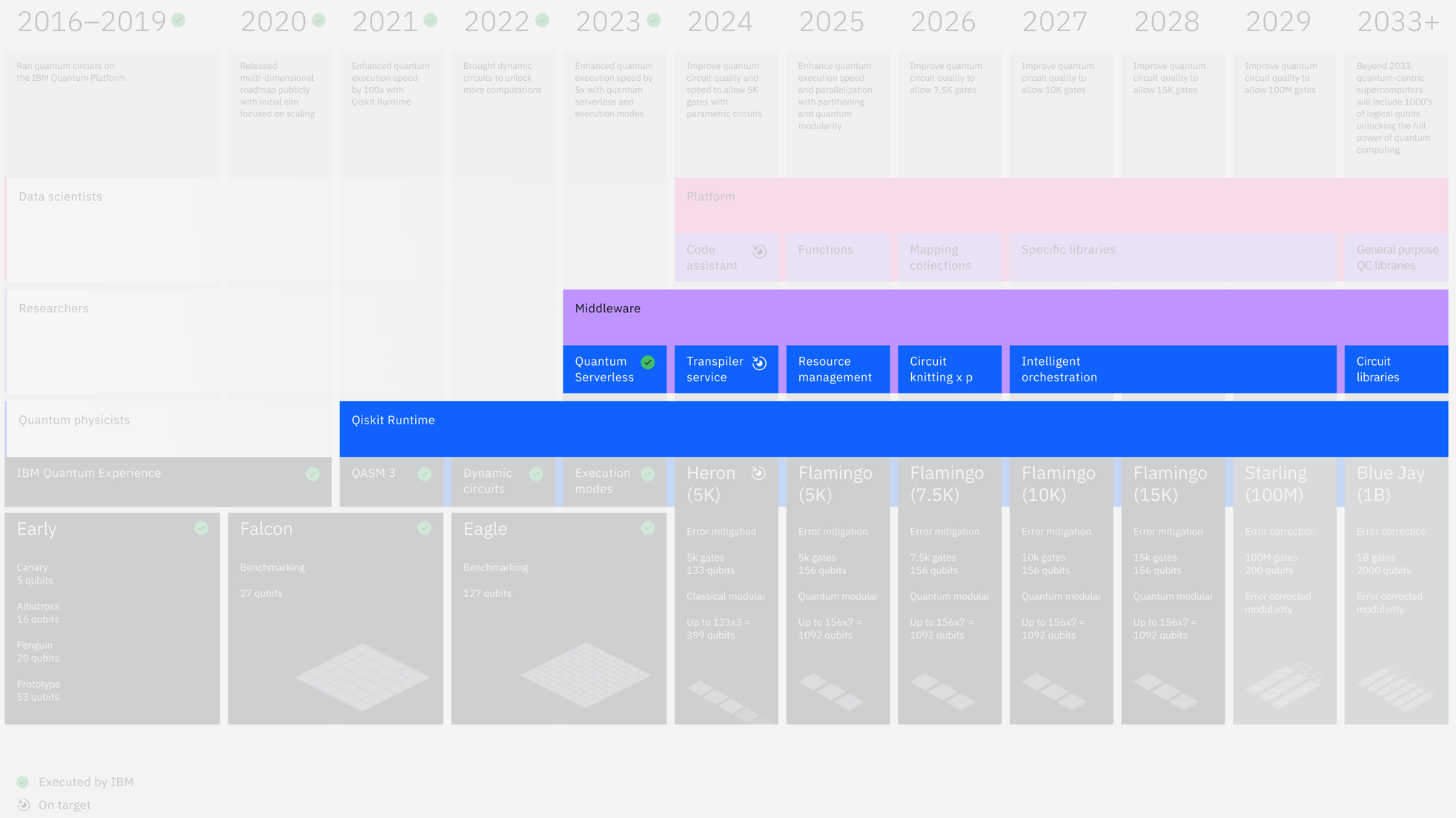
IBM **Quantum**

In 2024, our AI-powered transpiler service will optimize circuits with fewer gates.

In 2025, we will introduce resource management tools to facilitate system partitioning and enable parallel execution.

2026 will bring us circuit knitting across parallel quantum processors—the ability to decompose quantum circuits into shorter circuits, run them in parallel, and then stitch them back together with classical hardware. Circuit knitting will bring performance gains and let you run complex algorithms sooner.

From 2027 onward, we will focus on intelligent orchestration: optimizing workflows to combine classical and quantum efficiently, thus improving performance.



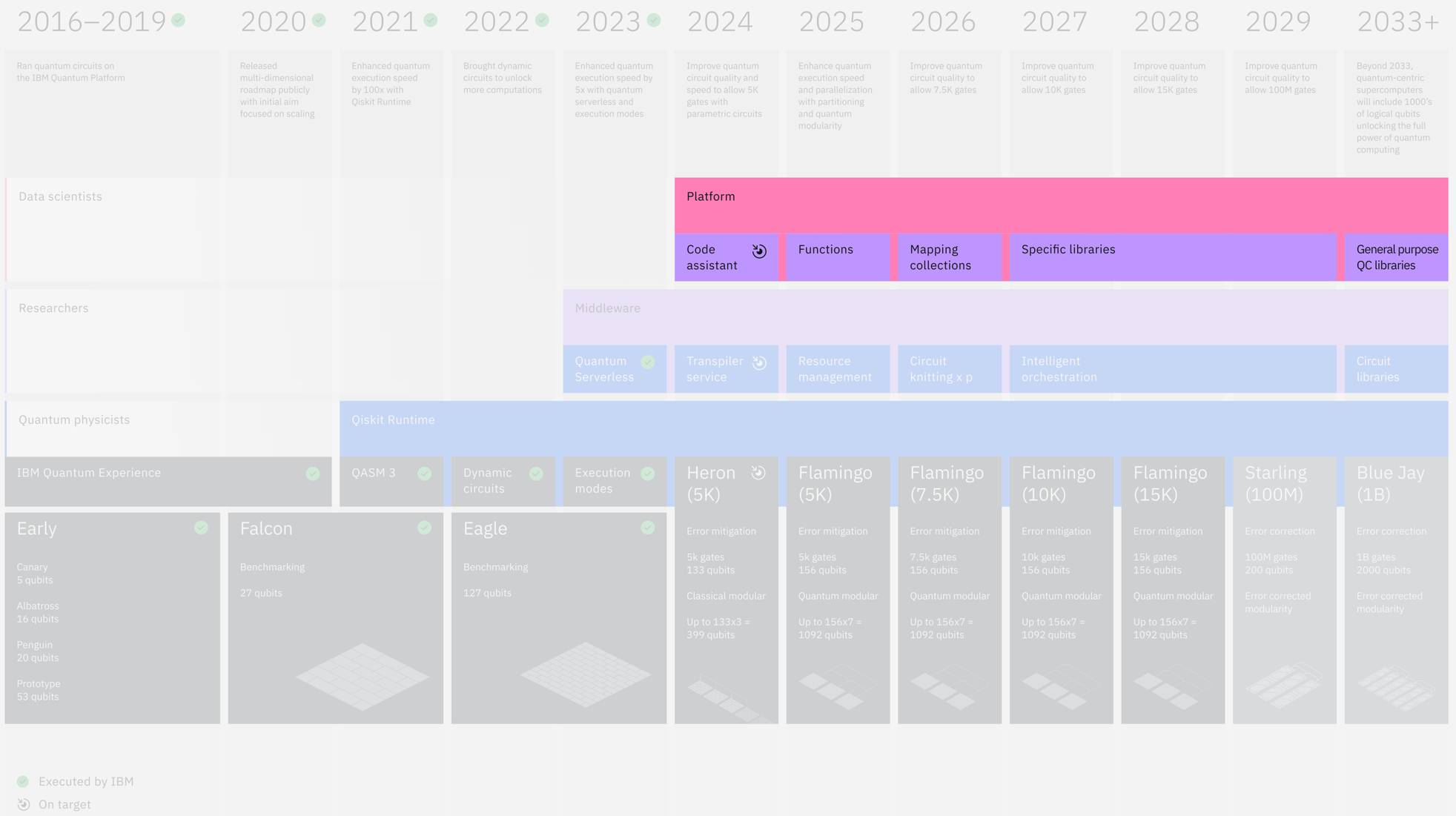
IBM Quantum / © 2024 IBM Corporation

What we have accomplished: Software

IBM **Quantum**

Useful quantum computing requires performant software. We're committed to maturing Qiskit into a software stack capable of running utility-scale circuits on cloud-based quantum resources. As we say, Qiskit + IBM Quantum systems = work.

In 2023, we aggregated Qiskit documentation and learning resources into the IBM Quantum Platform to create a single Qiskit source of truth.



Looking forward: Software

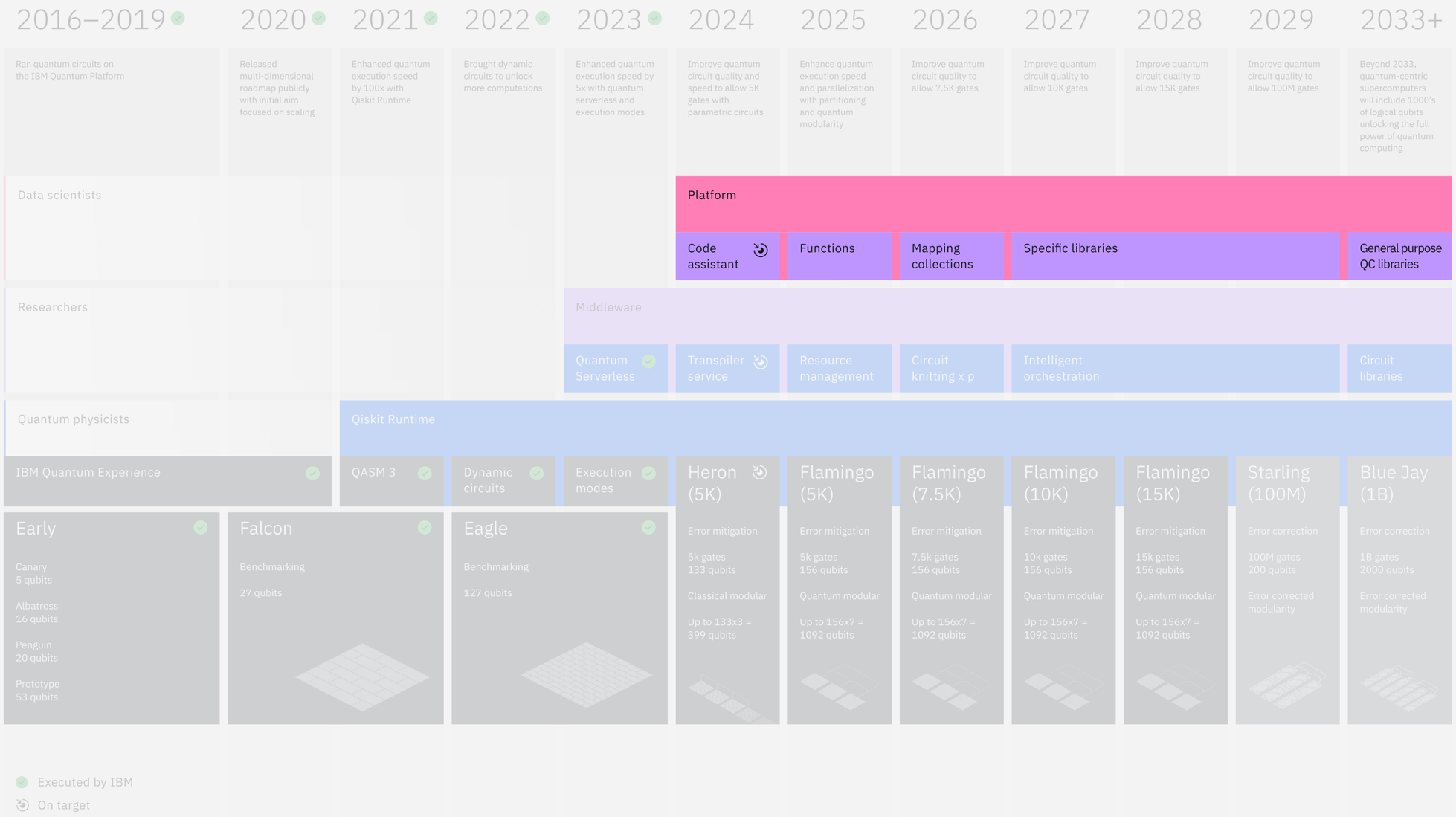
IBM **Quantum**

In 2025, we will introduce quantum functions so users can create and share reusable blocks of Qiskit code.

2026 will bring mapping collections so users can start automating the process of mapping their specific use cases to quantum circuits.

From 2027 onward, we will work alongside clients to build use-case-specific libraries as quantum advantages emerge for a variety of use cases.

By 2033, we expect to see general-purpose quantum computing libraries that users can incorporate into a wide variety of quantum applications.



IBM Quantum / © 2024 IBM Corporation

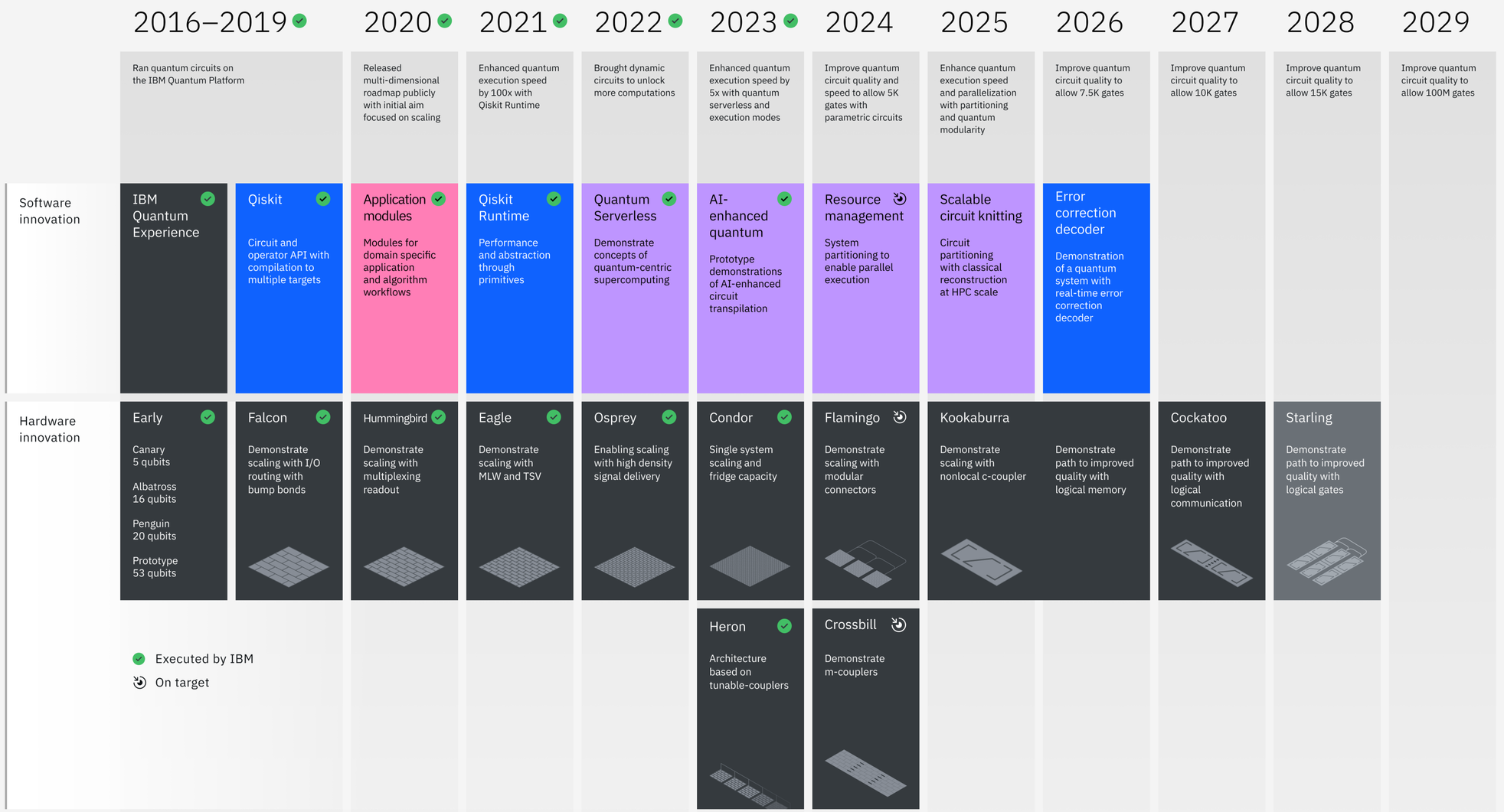
Innovation roadmap

IBM **Quantum**

We remain committed to the transparent development of IBM quantum hardware and software. This includes showing off scientific discoveries required to clear roadblocks in the field.

Therefore, in 2023, we also announced our innovation roadmap. This roadmap features internal releases of hardware and software to enable the subsequent milestones on our development roadmap.

Some technologies on our innovation roadmap will be internal proofs-of-concept to inform future development. Others will be prototypes for eventual release.



IBM Quantum / © 2024 IBM Corporation

Innovation roadmap

IBM **Quantum**

Our hardware innovations focus on building interconnects that allow us to scale processors and parallelize quantum workloads while laying a foundation for quantum error correction.

In 2024, we will demonstrate m-couplers to seam chips together and l-couplers to connect chips over longer distances with Crossbill and Flamingo, respectively.

In 2025 and 2026, we will develop c-couplers capable of linking distant qubits on the same chip as required by error correction schemes for a concept called Kookaburra.

2027 and 2028 further pave a path to error correction. Cockatoo will debut logical communication and Starling will be able to run logical gates on error-corrected logical qubits.

Innovation roadmap

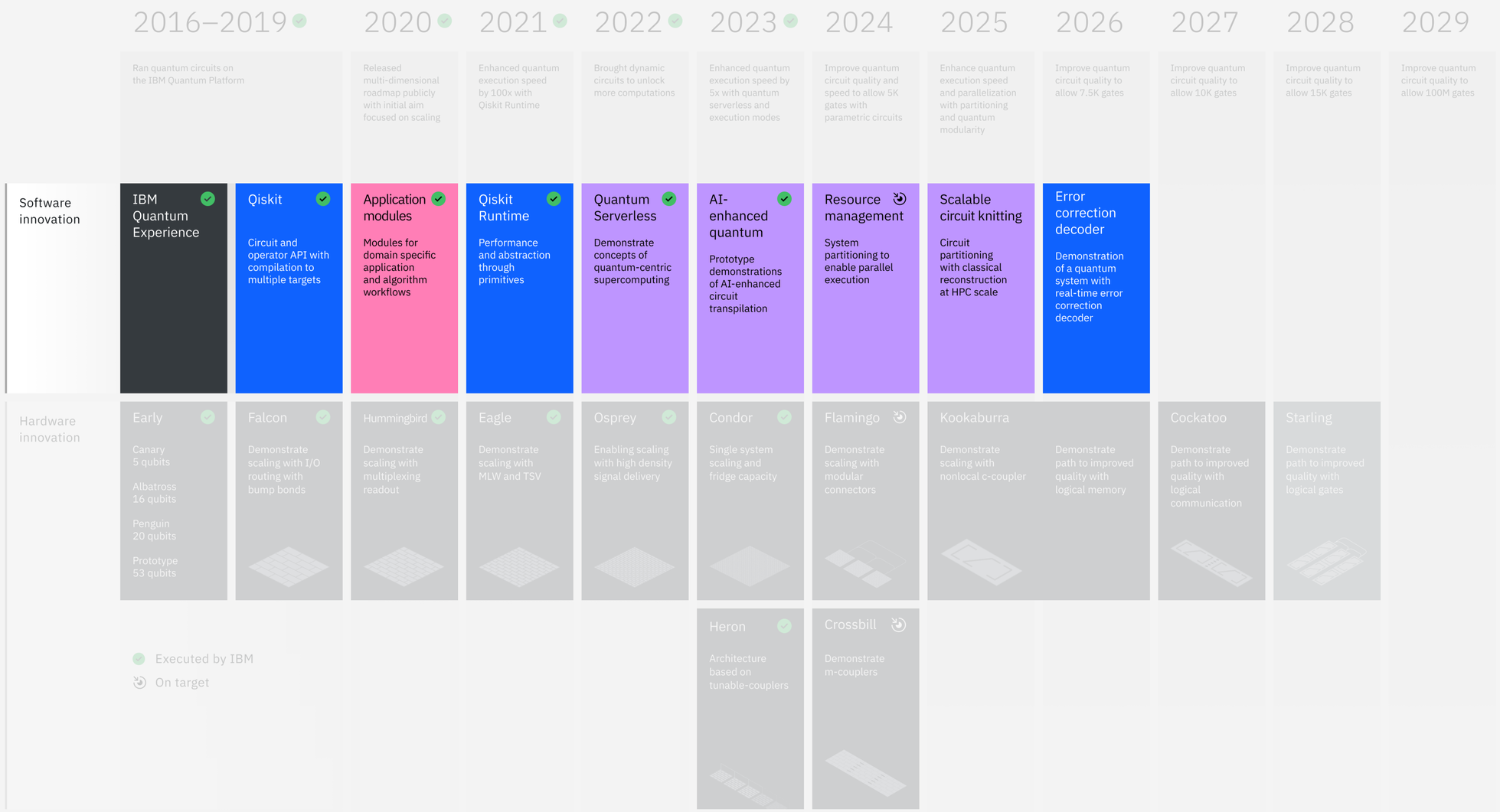
IBM **Quantum**

Our software innovations will support the execution of large circuits on modular quantum computers and build the tools for a frictionless developer experience, rising to the Development Roadmap in the following years.

In 2023, we showed our plan to incorporate AI into quantum computing workflows with AI-assisted circuit transpilation.

In 2024 and 2025, we will prototype new tools for resource management and scalable circuit knitting for parallel execution and classical reconstruction of circuits at the HPC scale.

In 2026, we will prototype a real-time error correction decoder for later error corrected systems.



IBM Quantum / © 2024 IBM Corporation

Development Roadmap

IBM **Quantum**

	2016–2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2033+
	Ran quantum circuits on the IBM Quantum Platform	Released multi-dimensional roadmap publicly with initial aim focused on scaling	Enhanced quantum execution speed by 100x with Qiskit Runtime	Brought dynamic circuits to unlock more computations	Enhanced quantum execution speed by 5x with Quantum Serverless and execution modes	Improve quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhance quantum execution speed and parallelization with partitioning and quantum modularity	Improve quantum circuit quality to allow 7.5K gates	Improve quantum circuit quality to allow 10K gates	Improve quantum circuit quality to allow 15K gates	Improve quantum circuit quality to allow 100M gates	Beyond 2033, quantum-centric supercomputers will include 1000's of logical qubits unlocking the full power of quantum computing
Data scientists						Platform						
						Code assistant	Functions	Mapping collections	Specific libraries			General purpose QC libraries
Researchers					Middleware							
					Quantum Serverless	Transpiler service	Resource management	Circuit knitting x p	Intelligent orchestration			Circuit libraries
Quantum physicists			Qiskit Runtime									
	IBM Quantum Experience		QASM 3	Dynamic circuits	Execution modes	Heron (5K)	Flamingo (5K)	Flamingo (7.5K)	Flamingo (10K)	Flamingo (15K)	Starling (100M)	Blue Jay (1B)
	Early Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	**Falcon** Benchmarking 27 qubits		**Eagle** Benchmarking 127 qubits		Error mitigation 5k gates 133 qubits Classical modular 133x3 = 399 qubits	Error mitigation 5k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 7.5k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 10k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 15k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error correction 100M gates 200 qubits Error corrected modularity	Error correction 1B gates 2000 qubits Error corrected modularity

Innovation Roadmap

Software innovation	IBM Quantum Experience	Qiskit Circuit and operator API with compilation to multiple targets	Application modules Modules for domain specific application and algorithm workflows	Qiskit Runtime Performance and abstraction through primitives	Quantum Serverless Demonstrate concepts of quantum-centric supercomputing	AI-enhanced quantum Prototype demonstrations of AI-enhanced circuit transpilation	Resource management System partitioning to enable parallel execution	Scalable circuit knitting Circuit partitioning with classical reconstruction at HPC scale	Error correction decoder Demonstration of a quantum system with real-time error correction decoder			
Hardware innovation	**Early** Canary 5 qubits Penguin 20 qubits Albatross 16 qubits Prototype 53 qubits	**Falcon** Demonstrate scaling with I/O routing with bump bonds	**Hummingbird** Demonstrate scaling with multiplexing readout	**Eagle** Demonstrate scaling with MLW and TSV	**Osprey** Enabling scaling with high density signal delivery	**Condor** Single system scaling and fridge capacity	**Flamingo** Demonstrate scaling with modular connectors	**Kookaburra** Demonstrate scaling with nonlocal c-coupler Demonstrate path to improved quality with logical memory	**Cockatoo** Demonstrate path to improved quality with logical communication	**Starling** Demonstrate path to improved quality with logical gates		
						Heron Architecture based on tunable-couplers	**Crossbill** Demonstrate m-couplers					

IBM Quantum / © 2024 IBM Corporation